UNITED STATES PATENT AND TRADEMARK OFFICE **CERTIFICATE OF CORRECTION**

PATENT NO.

: 6,781,232 B2

APPLICATION NO.: 10/633829

DATED

: August 24, 2004

INVENTOR(S)

: Joseph I. Rubin

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title page showing illustrative figure, should be deleted and susbtitute therefor the attached title page.

Signed and Sealed this

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First Day of May, 2007

JON W. DUDAS
Director of the United States Patent and Trademark Office

(12) United States Patent Rubin

US 6,781,232 B2 (10) Patent No.: (45) Date of Patent: Aug. 24, 2004

(54) SAMPLE PREPARATION APPARATUS AND METHOD

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(*) Notice: Subject to any disclaimer, the term of this patent is extended or adjusted under 35 U.S.C. 154(b) by 0 days.

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` '	2001, now Pal. No. 6,6	30,36	9.			•

(51)	Int. Cl.7		H01L 23/10
(52)	U.S. Cl.	257	//707: 438/15

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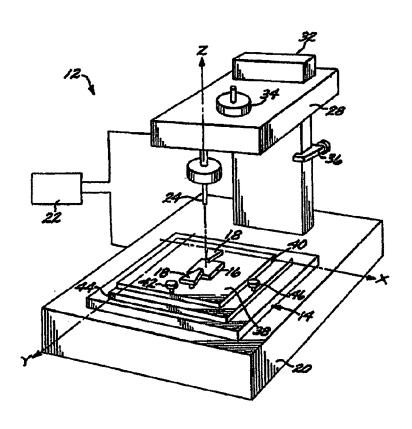
* cited by examiner

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ABSTRACT

A apparatus and method for forming windows in semiconductor devices to enable visualization of the circuitry therein while electrically intact. The device is affixed to a table that is oscillated in the X and Y directions while a succession of rotating tools are brought to bear against the surface of the device in the Z direction under a constant force. The force is adjustable so as to allow the tool to float on the surface of the workpiece.

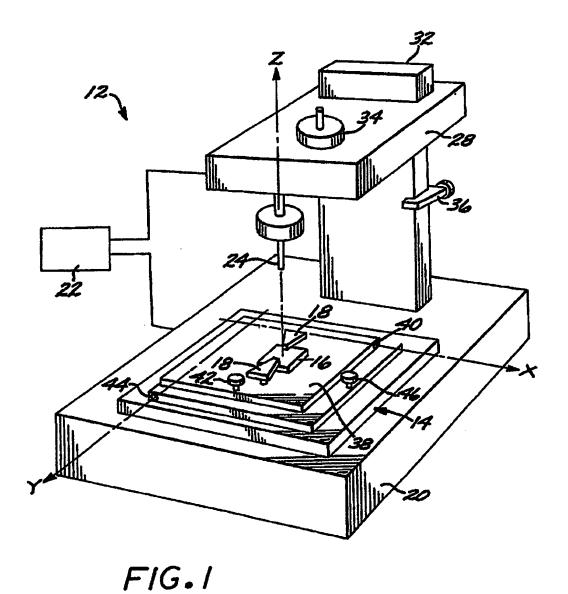
5 Claims, 3 Drawing Sheets



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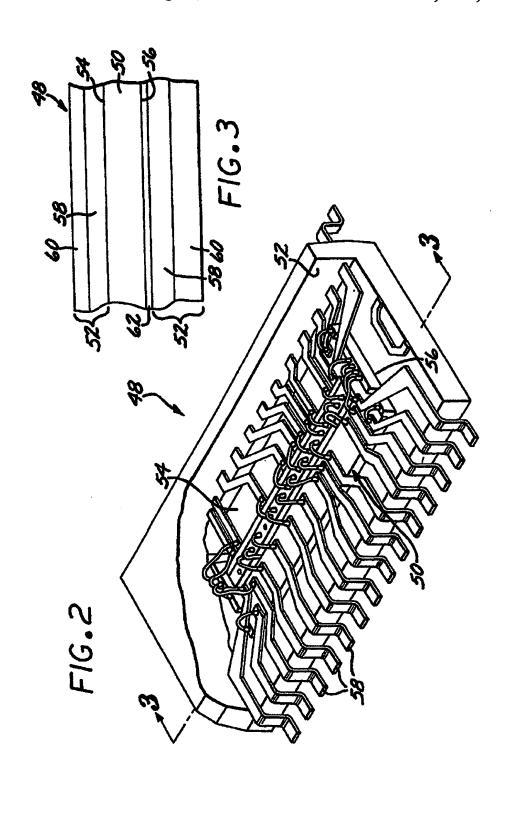
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